

LM20

LM20 2.4V, 10µA, SC70, micro SMD Temperature Sensor

Check for Samples: LM20

FEATURES

- Rated for full -55°C to +130°C range
- Available in an SC70 and micro SMD package
- Predictable curvature error
- Suitable for remote applications

APPLICATIONS

- Cellular Phones
- Computers
- Power Supply Modules
- Battery Management
- FAX Machines
- Printers
- HVAC
- Disk Drives
- Appliances

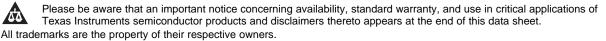
DESCRIPTION

The LM20 is a precision analog output CMOS integrated-circuit temperature sensor that operates over a -55° C to $+130^{\circ}$ C temperature range. The power supply operating range is +2.4 V to +5.5 V. The transfer function of LM20 is predominately linear, yet has a slight predictable parabolic curvature. The accuracy of the LM20 when specified to a parabolic transfer function is $\pm 1.5^{\circ}$ C at an ambient temperature of $+30^{\circ}$ C. The temperature error increases linearly and reaches a maximum of $\pm 2.5^{\circ}$ C at the temperature range extremes. The temperature range is affected by the power supply voltage. At a power supply voltage of 2.7 V to 5.5 V the temperature range extremes are $+130^{\circ}$ C and -55° C. Decreasing the power supply voltage to 2.4 V changes the negative extreme to -30° C, while the positive remains at $+130^{\circ}$ C.

The LM20's quiescent current is less than 10 μ A. Therefore, self-heating is less than 0.02°C in still air. Shutdown capability for the LM20 is intrinsic because its inherent low power consumption allows it to be powered directly from the output of many logic gates or does not necessitate shutdown at all.

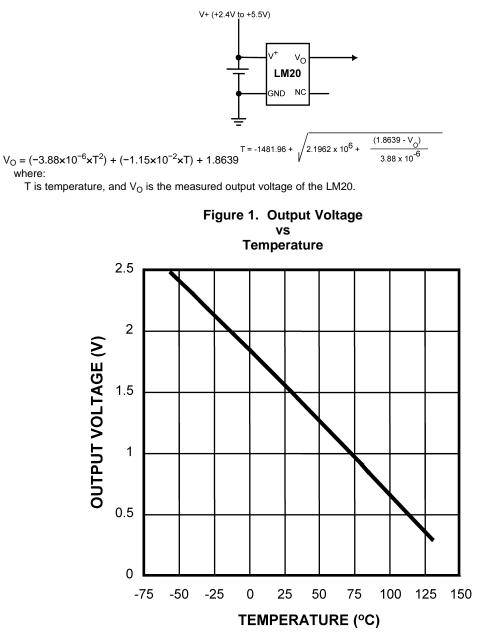
Table 1. Key Specifications

	VALUE	UNIT
Accuracy at +30°C	±1.5 to ±4	°C (max)
Accuracy at +130°C & −55°C	±2.5 to ±5	°C (max)
Power Supply Voltage Range	+2.4V to +5.5	V
Current Drain	10	μA (max)
Nonlinearity	±0.4 % (typ)	
Output Impedance	160	Ω (max)
Load Regulation 0 μ A < I _L < +16 μ A	-2.5	mV (max)



TYPICAL APPLICATION

FULL-RANGE CELSIUS (CENTIGRADE) TEMPERATURE SENSOR (-55°C TO +130°C) OPERATING FROM A SINGLE LI-ION BATTERY CELL

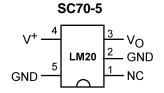




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Temperature (T)	Typical V _O
+130°C	+303 mV
+100°C	+675 mV
+80°C	+919 mV
+30°C	+1515 mV
+25°C	+1574 mV
0°C	+1863.9 mV
-30°C	+2205 mV
-40°C	+2318 mV
-55°C	+2485 mV

CONNECTION DIAGRAM



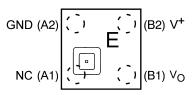
Note:

- GND (pin 2) may be grounded or left floating. For optimum thermal conductivity to the pc board ground plane pin 2 should be grounded.

- NC (pin 1) should be left floating or grounded. Other signal traces should not be connected to this pin.

Figure 2. Top View See NS Package Number MAA05A

micro SMD



Note:

- Pin numbers are referenced to the package marking text orientation.
- Reference JEDEC Registration MO-211, variation BA

- The actual physical placement of package marking will vary slightly from part to part. The package marking will designate the date code and will vary considerably. Package marking does not correlate to device type in any way.

Figure 3. Top View See NS Package Number TLA04ZZA



These devices have limited built-in ESD protection. The leads should be shorted together or the device placed in conductive foam during storage or handling to prevent electrostatic damage to the MOS gates.



ABSOLUTE MAXIMUM RATINGS⁽¹⁾

Supply Voltage	+6.5V to -0.2V
Output Voltage	(V ⁺ + 0.6 V) to −0.6 V
Output Current	10 mA
Input Current at any pin (2)	5 mA
Storage Temperature	−65°C to +150°C
Maximum Junction Temperature (T _{JMAX})	+150°C
ESD Susceptibility (3)	
Human Body Model	2500 V
Machine Model	250 V
Soldering process must comply with National's Reflow Temperature Profile specifications. Refer to www.national.com/packaging.	.(4)

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) When the input voltage (V_I) at any pin exceeds power supplies (V_I < GND or V_I > V⁺), the current at that pin should be limited to 5 mA.
(3) The human body model is a 100 pF capacitor discharged through a 1.5 kΩ resistor into each pin. The machine model is a 200 pF capacitor discharged directly into each pin.

(4) Reflow temperature profiles are different for lead-free and non-lead-free packages.

OPERATING RATINGS⁽¹⁾

Specified Temperature Range:	$T_{MIN} \leq T_{A} \leq T_{MAX}$
LM20B, LM20C with 2.4 V ≤ V ⁺ ≤ 2.7 V	-30°C ≤ T _A ≤ +130°C
LM20B, LM20C with 2.7 V ≤ V ⁺ ≤ 5.5 V	−55°C ≤ T _A ≤ +130°C
LM20S with 2.4 V ≤ V ⁺ ≤ 5.5 V	-30°C ≤ T _A ≤ +125°C
LM20S with 2.7 V ≤ V ⁺ ≤ 5.5 V	-40°C ≤ T _A ≤ +125°C
Supply Voltage Range (V ⁺)	+2.4 V to +5.5 V
Thermal Resistance, $\theta_{JA}^{(2)}$ SC-70 micro SMD	415°C/W 340°C/W

(1) Absolute Maximum Ratings indicate limits beyond which damage to the device may occur. Operating Ratings indicate conditions for which the device is functional, but do not guarantee specific performance limits. For guaranteed specifications and test conditions, see the Electrical Characteristics. The guaranteed specifications apply only for the test conditions listed. Some performance characteristics may degrade when the device is not operated under the listed test conditions.

(2) The junction to ambient thermal resistance (θ_{JA}) is specified without a heat sink in still air using the printed circuit board layout shown in PCB LAYOUTS USED FOR THERMAL MEASUREMENTS.



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LM20

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ELECTRICAL CHARACTERISTICS

Unless otherwise noted, these specifications apply for $V^+ = +2.7 V_{DC}$. Boldface limits apply for $T_A = T_J = T_{MIN}$ to T_{MAX} ; all other limits $T_A = T_J = 25^{\circ}$ C; Unless otherwise noted.

			LM20B	LM20C	LM20S	
Parameter	Conditions	Typical	Limits	Limits	Limits	Units (Limit)
			(2)	(2)	(2)	(Emm)
	$T_A = +25^{\circ}C \text{ to } +30^{\circ}C$		±1.5	±4.0	±2.5	°C (max)
	T _A = +130°C		±2.5	±5.0		°C (max)
	T _A = +125°C		±2.5	±5.0	±3.5	°C (max)
Temperature to Voltage Error	T _A = +100°C		±2.2	±4.7	±3.2	°C (max)
$V_{O} = (-3.88 \times 10^{-6} \times T^{2})$ + (-1.15×10 ⁻² ×T) + 1.8639V	T _A = +85°C		±2.1	±4.6	±3.1	°C (max)
(3)	$T_A = +80^{\circ}C$		±2.0	±4.5	±3.0	°C (max)
	$T_A = 0^{\circ}C$		±1.9	±4.4	±2.9	°C (max)
	T _A = −30°C		±2.2	±4.7	±3.3	°C (min)
	$T_A = -40^{\circ}C$		±2.3	±4.8	±3.5	°C (max)
	T _A = −55°C		±2.5	±5.0		°C (max)
Output Voltage at 0°C		+1.8639				V
Variance from Curve		±1.0				°C
Non-Linearity (4)	$-20^{\circ}C \le T_A \le +80^{\circ}C$	±0.4				%
Sensor Gain (Temperature Sensitivity or Average Slope) to equation: $V_O=-11.77$ mV/°C×T+1.860V	$-30^{\circ}\text{C} \le \text{T}_{\text{A}} \le +100^{\circ}\text{C}$	-11.77	-11.4 -12.2	-11.0 -12.6	-11.0 -12.6	mV/°C (min) mV/°C (max)
Output Impedance	$0 \ \mu A \le I_L \le +16 \ \mu A$		160	160	160	Ω (max)
Load Regulation ⁽⁷⁾	$0 \ \mu A \le I_L \le +16 \ \mu A$		-2.5	-2.5	-2.5	mV (max)
Line Regulation ⁽⁸⁾	+2. 4 V ≤ V ⁺ ≤ +5.0V		+3.3	+3.7	+3.7	mV/V (max)
	$+5.0 \text{ V} \le \text{V}^+ \le +5.5 \text{ V}$		+11	+11	+11	mV (max)
	+2. $4V \le V^+ \le +5.0V$	4.5	7	7	7	µA (max)
Quiescent Current	$+5.0V \le V^+ \le +5.5V$	4.5	9	9	9	µA (max)
	+2. 4V ≤ V ⁺ ≤ +5.0V	4.5	10	10	10	μA (max)
Change of Quiescent Current	+2. 4 V ≤ V ⁺ ≤ +5.5V	+0.7				μA
Temperature Coefficient of		-11				nA/°C
Quiescent Current						
Shutdown Current	V ⁺ ≤ +0.8 V	0.02				μA

(1) Typicals are at $T_J = T_A = 25^{\circ}C$ and represent most likely parametric norm.

(2) Limits are guaranteed to National's AOQL (Average Outgoing Quality Level).

(3) Accuracy is defined as the error between the measured and calculated output voltage at the specified conditions of voltage, current, and temperature (expressed in °C).

(4) Non-Linearity is defined as the deviation of the calculated output-voltage-versus-temperature curve from the best-fit straight line, over the temperature range specified.

(5) Negative currents are flowing into the LM20. Positive currents are flowing out of the LM20. Using this convention the LM20 can at most sink -1 μA and source +16 μA.

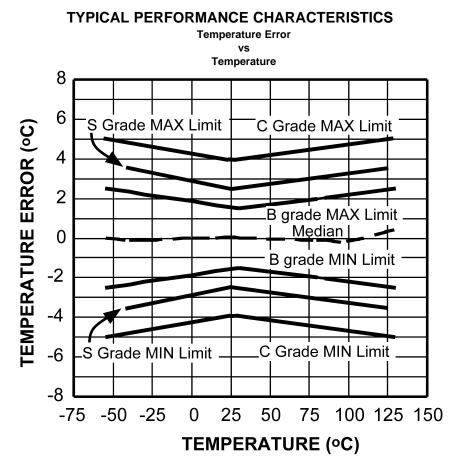
(6) Load regulation or output impedance specifications apply over the supply voltage range of +2.4V to +5.5V.

(7) Regulation is measured at constant junction temperature, using pulse testing with a low duty cycle. Changes in output due to heating effects can be computed by multiplying the internal dissipation by the thermal resistance.

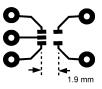
(8) Line regulation is calculated by subtracting the output voltage at the highest supply input voltage from the output voltage at the lowest supply input voltage.

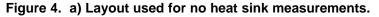


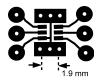
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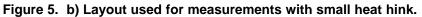


PCB LAYOUTS USED FOR THERMAL MEASUREMENTS









LM20 TRANSFER FUNCTION

The LM20's transfer function can be described in different ways with varying levels of precision. A simple linear transfer function, with good accuracy near 25°C, is

 $V_0 = -11.69 \text{ mV/}^{\circ}\text{C} \times \text{T} + 1.8663 \text{ V}$

(1)

Over the full operating temperature range of -55° C to $+130^{\circ}$ C, best accuracy can be obtained by using the parabolic transfer function.

 $V_{\rm O} = (-3.88 \times 10^{-6} \times T^2) + (-1.15 \times 10^{-2} \times T) + 1.8639$

(2)



solving for T:

$$T = -1481.96 + \sqrt{2.1962 \times 10^6 + \frac{(1.8639 - V_0)}{3.88 \times 10^{-6}}}$$

A linear transfer function can be used over a limited temperature range by calculating a slope and offset that give best results over that range. A linear transfer function can be calculated from the parabolic transfer function of the LM20. The slope of the linear transfer function can be calculated using the following equation:

$$m = -7.76 \times 10^{-6} \times T = 0.0115$$
,

where T is the middle of the temperature range of interest and m is in V/°C. For example for the temperature range of $T_{MIN} = -30$ to $T_{MAX} = +100^{\circ}$ C:

 $T = 35^{\circ}C$

and

m = -11.77 mV/°C

The offset of the linear transfer function can be calculated using the following equation:

 $b = (V_{OP}(T_{MAX}) + V_{OP}(T) - m \times (T_{MAX}+T))/2$

where:

- $V_{OP}(T_{MAX})$ is the calculated output voltage at T_{MAX} using the parabolic transfer function for V_{OP}
- $V_{OP}(T)$ is the calculated output voltage at T using the parabolic transfer function for V_{O} .

Using this procedure the best fit linear transfer function for many popular temperature ranges was calculated in Table 2. As shown in Table 2 the error that is introduced by the linear transfer function increases with wider temperature ranges.

Table 2. First order equations optimized for different temperature ranges

Temperature Range		Linear Equation V _O =	Maximum Deviation of Linear Equation from Parabolic Equation (°C)
T _{min} (°C)	T _{max} (°C)		
-55	+130	−11.79 mV/°C × T + 1.8528 V	±1.41
-40	+110	−11.77 mV/°C × T + 1.8577 V	±0.93
-30	+100	−11.77 mV/°C × T + 1.8605 V	±0.70
-40	+85	−11.67 mV/°C × T + 1.8583 V	±0.65
-10	+65	−11.71 mV/°C × T + 1.8641 V	±0.23
+35	+45	−11.81 mV/°C × T + 1.8701 V	±0.004
+20	+30	−11.69 mV/°C × T + 1.8663 V	±0.004

MOUNTING

The LM20 can be applied easily in the same way as other integrated-circuit temperature sensors. It can be glued or cemented to a surface. The temperature that the LM20 is sensing will be within about +0.02°C of the surface temperature to which the LM20's leads are attached to.

This presumes that the ambient air temperature is almost the same as the surface temperature; if the air temperature were much higher or lower than the surface temperature, the actual temperature measured would be at an intermediate temperature between the surface temperature and the air temperature.

To ensure good thermal conductivity the backside of the LM20 die is directly attached to the pin 2 GND pin. The tempertures of the lands and traces to the other leads of the LM20 will also affect the temperature that is being sensed.

Alternatively, the LM20 can be mounted inside a sealed-end metal tube, and can then be dipped into a bath or screwed into a threaded hole in a tank. As with any IC, the LM20 and accompanying wiring and circuits must be kept insulated and dry, to avoid leakage and corrosion. This is especially true if the circuit may operate at cold temperatures where condensation can occur. Printed-circuit coatings and varnishes such as Humiseal and epoxy paints or dips are often used to ensure that moisture cannot corrode the LM20 or its connections.

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The thermal resistance junction to ambient (θ_{JA}) is the parameter used to calculate the rise of a device junction temperature due to its power dissipation. For the LM20 the equation used to calculate the rise in the die temperature is as follows:

$$\mathsf{T}_\mathsf{J} = \mathsf{T}_\mathsf{A} + \boldsymbol{\theta}_\mathsf{J}_\mathsf{A} \left[(\mathsf{V}^+ \; \mathsf{I}_\mathsf{Q}) + (\mathsf{V}^+ - \mathsf{V}_\mathsf{O}) \; \mathsf{I}_\mathsf{L} \right]$$

where I_Q is the quiescent current and I_L is the load current on the output. Since the LM20's junction temperature is the actual temperature being measured care should be taken to minimize the load current that the LM20 is required to drive.

The tables shown in Table 3 summarize the rise in die temperature of the LM20 without any loading, and the thermal resistance for different conditions.

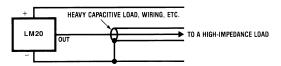
	so	270-5	SC70-5			
	no heat sink		small heat sink			
	θ_{JA} $T_J - T_A$		θ _{JA}	$T_J - T_A$		
	(°C/W)	(°C)	(°C/W)	(°C)		
Still air	412	0.2	350	0.19		
Moving air	312	0.17	266	0.15		

(1) See PCB LAYOUTS USED FOR THERMAL MEASUREMENTS for PCB layout samples.

	micr	o SMD	micro SMD		
	no heat sink		small heat fin		
	θ _{JA}	θ _{JA} T _J – T _A		T _J – T _A	
	(°C/W)	(°C)	(°C/W)	(°C)	
Still air	340	0.18	TBD	TBD	
Moving air	TBD	TBD	TBD	TBD	

CAPACITIVE LOADS

The LM20 handles capacitive loading well. Without any precautions, the LM20 can drive any capacitive load less than 300 pF as shown in Figure 6. Over the specified temperature range the LM20 has a maximum output impedance of 160 Ω . In an extremely noisy environment it may be necessary to add some filtering to minimize noise pickup. It is recommended that 0.1 µF be added from V⁺ to GND to bypass the power supply voltage, as shown in Figure 7. In a noisy environment it may even be necessary to add a capacitor from the output to ground with a series resistor as shown in Figure 7. A 1 µF output capacitor with the 160 Ω maximum output impedance and a 200 Ω series resistor will form a 442 Hz lowpass filter. Since the thermal time constant of the LM20 is much slower, the overall response time of the LM20 will not be significantly affected.



Fiaure 6.	LM20 No Decoupling	a Required for	Capacitive Load	s Less than 300 pF
		5	Capacitite Load	

R (Ω)	C (μF)
200	1
470	0.1
680	0.01
1 k	0.001



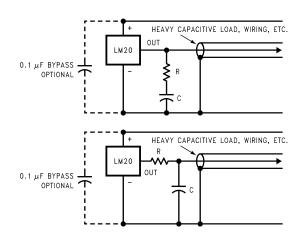
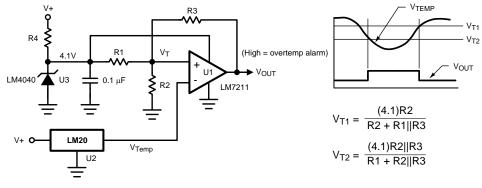


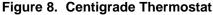
Figure 7. LM20 with Filter for Noisy Environment and Capacitive Loading greater than 300 pF. Either placement of resistor as shown above is just as effective

LM20 MICRO SMD LIGHT SENSITIVITY

Exposing the LM20 micro SMD package to bright sunlight may cause the output reading of the LM20 to drop by 1.5V. In a normal office environment of fluorescent lighting the output voltage is minimally affected (less than a millivolt drop). In either case it is recommended that the LM20 micro SMD be placed inside an enclosure of some type that minimizes its light exposure. Most chassis provide more than ample protection. The LM20 does not sustain permanent damage from light exposure. Removing the light source will cause LM20's output voltage to recover to the proper value.

APPLICATIONS CIRCUITS





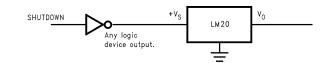


Figure 9. Conserving Power Dissipation with Shutdown



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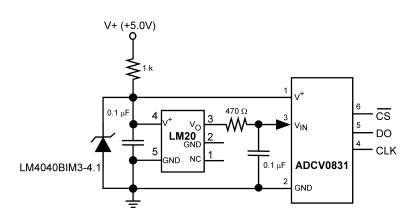


Figure 10. Suggested Connection to a Sampling Analog to Digital Converter Input Stage

Most CMOS ADCs found in ASICs have a sampled data comparator input structure that is notorious for causing grief to analog output devices such as the LM20 and many op amps. The cause of this grief is the requirement of instantaneous charge of the input sampling capacitor in the ADC. This requirement is easily accommodated by the addition of a capacitor. Since not all ADCs have identical input stages, the charge requirements will vary necessitating a different value of compensating capacitor. This ADC is shown as an example only. If a digital output temperature is required please refer to devices such as the LM74.



PACKAGING INFORMATION

Orderable Device	Status	Package Type	•	Pins	Package Qty	Eco Plan	Lead/Ball Finish	MSL Peak Temp	Samples
	(1)		Drawing			(2)		(3)	(Requires Login)
LM20BIM7	ACTIVE	SC70	DCK	5	1000	TBD	CU SNPB	Level-1-260C-UNLIM	
LM20BIM7/NOPB	ACTIVE	SC70	DCK	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM20BIM7X	ACTIVE	SC70	DCK	5	3000	TBD	CU SNPB	Level-1-260C-UNLIM	
LM20BIM7X/NOPB	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM20CIM7	ACTIVE	SC70	DCK	5	1000	TBD	CU SNPB	Level-1-260C-UNLIM	
LM20CIM7/NOPB	ACTIVE	SC70	DCK	5	1000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM20CIM7X	ACTIVE	SC70	DCK	5	3000	TBD	CU SNPB	Level-1-260C-UNLIM	
LM20CIM7X/NOPB	ACTIVE	SC70	DCK	5	3000	Green (RoHS & no Sb/Br)	CU SN	Level-1-260C-UNLIM	
LM20SITL/NOPB	ACTIVE	DSBGA	YZR	4	250	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	
LM20SITLX/NOPB	ACTIVE	DSBGA	YZR	4	3000	Green (RoHS & no Sb/Br)	SNAGCU	Level-1-260C-UNLIM	

⁽¹⁾ The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

OBSOLETE: TI has discontinued the production of the device.

(2) Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check http://www.ti.com/productcontent for the latest availability information and additional product content details.

TBD: The Pb-Free/Green conversion plan has not been defined.

Pb-Free (RoHS): TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

Pb-Free (RoHS Exempt): This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

Green (RoHS & no Sb/Br): TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

⁽³⁾ MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.





12-Nov-2012

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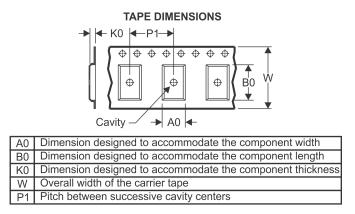
PACKAGE MATERIALS INFORMATION

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TAPE AND REEL INFORMATION





QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



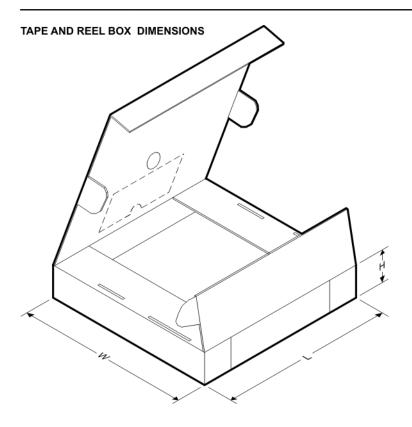
*All dimensions are nominal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
LM20BIM7	SC70	DCK	5	1000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20BIM7/NOPB	SC70	DCK	5	1000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20BIM7X	SC70	DCK	5	3000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20BIM7X/NOPB	SC70	DCK	5	3000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20CIM7	SC70	DCK	5	1000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20CIM7/NOPB	SC70	DCK	5	1000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20CIM7X	SC70	DCK	5	3000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20CIM7X/NOPB	SC70	DCK	5	3000	178.0	8.4	2.25	2.45	1.2	4.0	8.0	Q3
LM20SITL/NOPB	DSBGA	YZR	4	250	178.0	8.4	1.04	1.04	0.76	4.0	8.0	Q1
LM20SITLX/NOPB	DSBGA	YZR	4	3000	178.0	8.4	1.04	1.04	0.76	4.0	8.0	Q1

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PACKAGE MATERIALS INFORMATION

15-Nov-2012



*All dimensions are nominal							
Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
LM20BIM7	SC70	DCK	5	1000	203.0	190.0	41.0
LM20BIM7/NOPB	SC70	DCK	5	1000	203.0	190.0	41.0
LM20BIM7X	SC70	DCK	5	3000	206.0	191.0	90.0
LM20BIM7X/NOPB	SC70	DCK	5	3000	206.0	191.0	90.0
LM20CIM7	SC70	DCK	5	1000	203.0	190.0	41.0
LM20CIM7/NOPB	SC70	DCK	5	1000	203.0	190.0	41.0
LM20CIM7X	SC70	DCK	5	3000	206.0	191.0	90.0
LM20CIM7X/NOPB	SC70	DCK	5	3000	206.0	191.0	90.0
LM20SITL/NOPB	DSBGA	YZR	4	250	203.0	190.0	41.0
LM20SITLX/NOPB	DSBGA	YZR	4	3000	206.0	191.0	90.0

DCK (R-PDSO-G5)

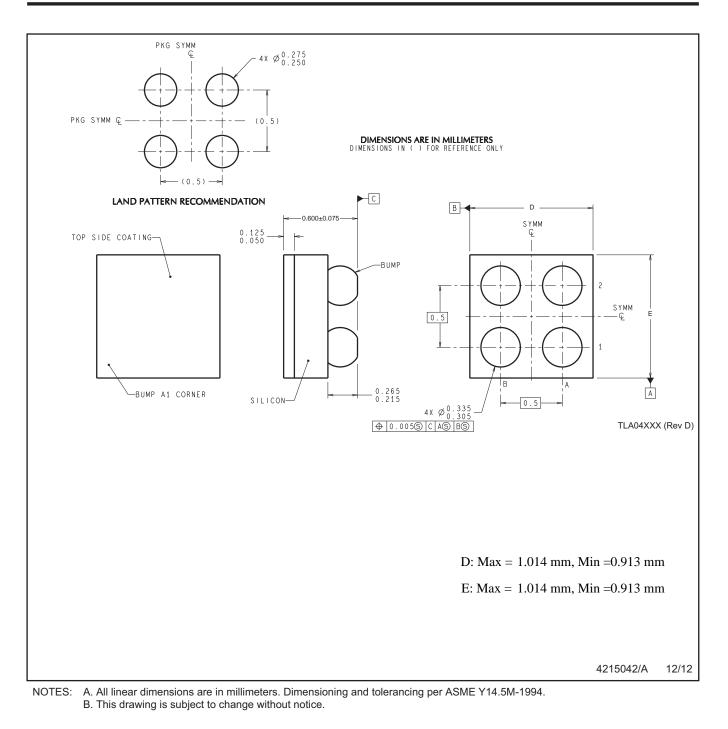
PLASTIC SMALL-OUTLINE PACKAGE



- NOTES: A. All linear dimensions are in millimeters.
 - B. This drawing is subject to change without notice.
 - C. Body dimensions do not include mold flash or protrusion. Mold flash and protrusion shall not exceed 0.15 per side.
 - D. Falls within JEDEC MO-203 variation AA.



YZR0004



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