2) Voltage protected to ±70V continuously, ±220V peak (pulsed at 1ms, 10% duty cycle max).

3) Resistors with values in parentheses are not installed.

4) In buffered mode, the protection resistor causes offset errors of $1nA \times 2.2k = 2.2uV$ on each input. These cancel for differential inputs.

5) VAS stands for Supply Voltage for the Analog Switch.

6) Digital and analog grounds are laid out in a star pattern.

7) Frame Grounds are used in single ended mode and the AGND & REF- are connected together.

8) Errors and possible latch up of the AD7714 may occur if the analog / reference inputs rise above 4.8 volts.

9) J1 selects 3.0 volt reference instead of 2.5 volt reference.

10. Errors and possible latch up of the AD7714 may occur if the analog / reference voltage is used in single ended mode and the AGND & REF- are connected together.

11. A ground loop will occur causing an offset error in A/D conversions if the AD7714 inputs rise above 4.8 volts.