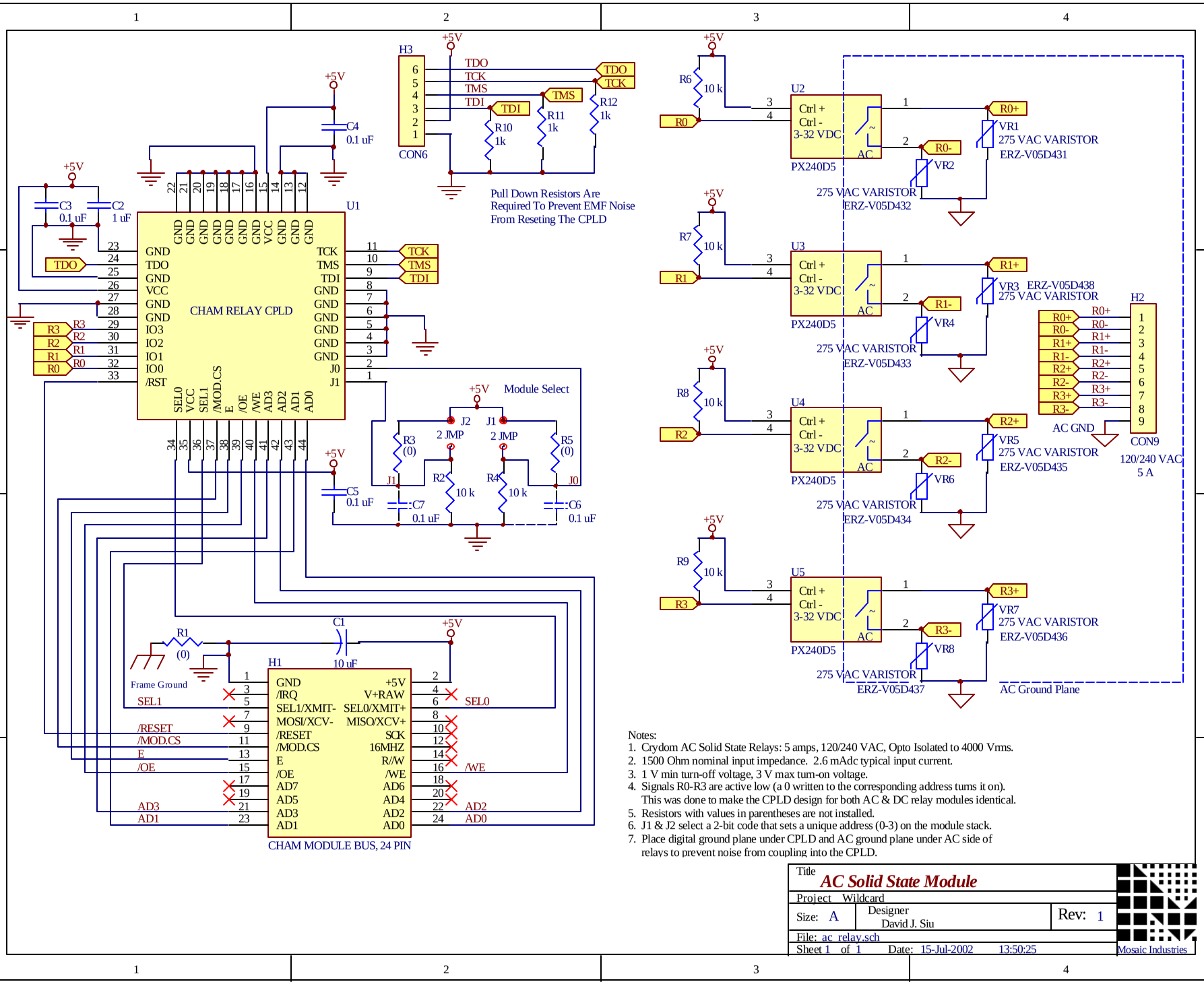

Notes:

1. Crydom AC Solid State Relays: 5 amps, 120/240 VAC, Opto Isolated to 4000 Vrms.
2. 1500 Ohm nominal input impedance. 2.6 mAdc typical input current.
3. 1 V min turn-off voltage, 3 V max turn-on voltage.
4. Signals R0-R3 are active low (a 0 written to the corresponding address turns it on). This was done to make the CPLD design for both AC & DC relay modules identical.
5. Resistors with values in parentheses are not installed.
6. J1 & J2 select a 2-bit code that sets a unique address (0-3) on the module stack.
7. Place digital ground plane under CPLD and AC ground plane under AC side of relays to prevent noise from coupling into the CPLD.

Title: **AC Solid State Module**		
Project: Wildcard		
Size: A	Designer: David J. Siu	Rev: 1
File: ac_relay.sch		
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