Analog Level Shifter III

APPLICATION NOTE MI-AN-026

Summary

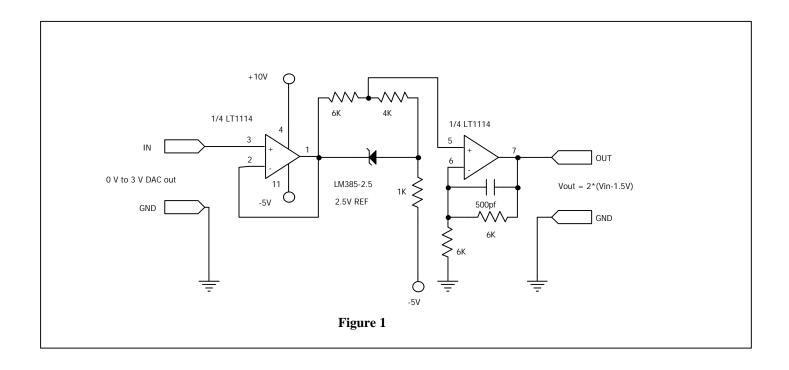
The following shows how to take a +0V to +3V output from the DAC and convert it to a -3V to +3V signal.

Description

The circuit below (Figure 1) takes a +0V to +3V output from the DAC and converts it to a -3V to +3V signal.

All connections are to the Analog I/O connector on the QED board. The negative supply must be at least -5V.

The LM385-2.5 is a precision voltage reference. The first stage of the circuit is a 1.5V level shifter (drop). The 6K and 4K resistors provide a voltage divider to get the 1V drop from the 2.5V reference and should be 1% value resistors or better. The second stage of the circuit is a 2X gain. The output of the circuit is 2*(Vin-1.5V)



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