Appendix C

Schematics

The following pages provide detailed schematics for the QScreen Controller. If you have any questions about the QScreen's electrical or mechanical characteristics please don't hesitate to contact us.

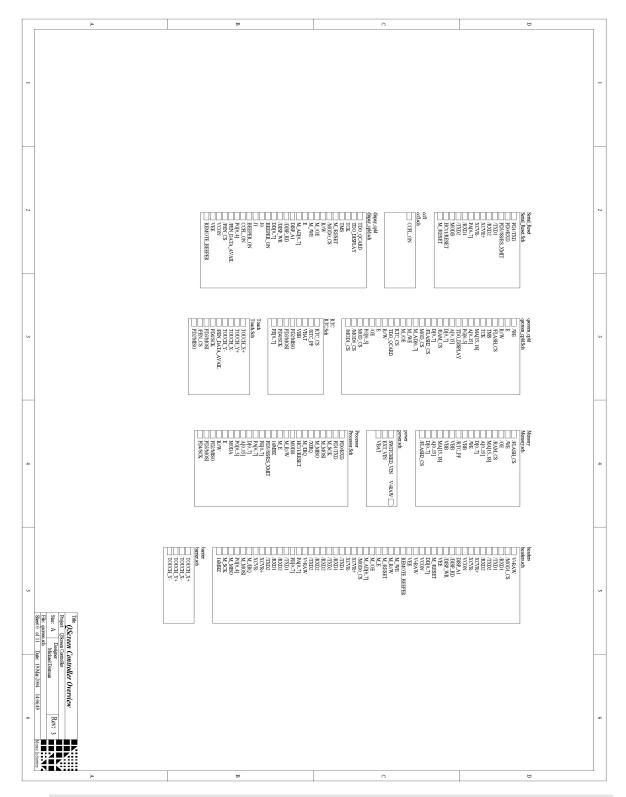


Figure C-1 QScreen Signal Directory.

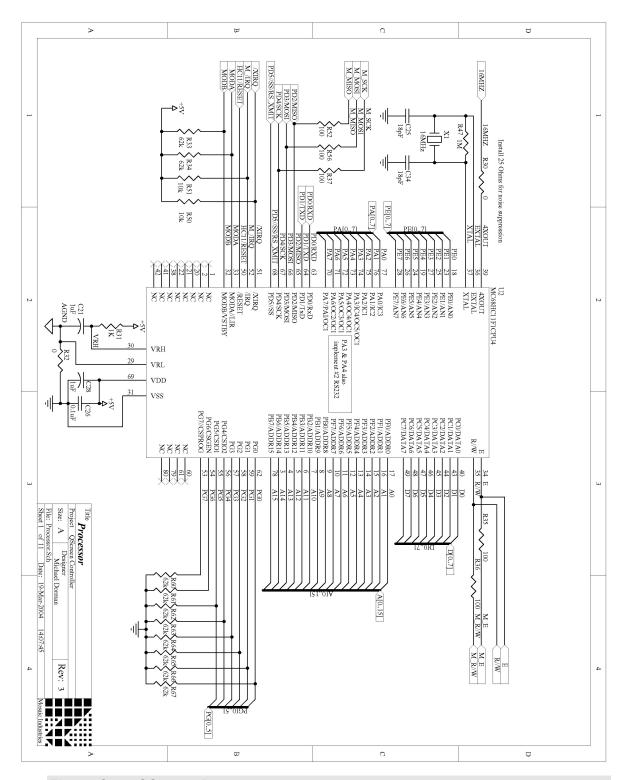


Figure C-2 QScreen Processor.

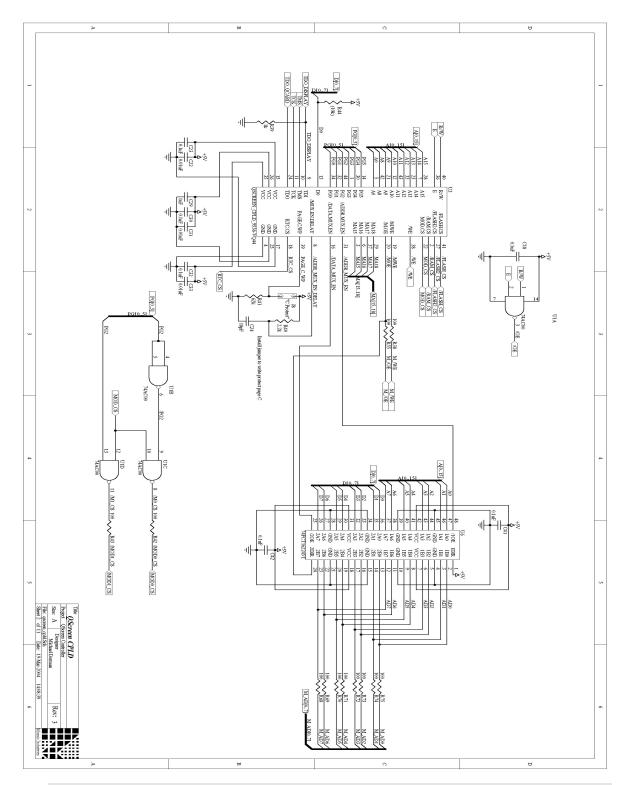


Figure C-3 QScreen CPLD.

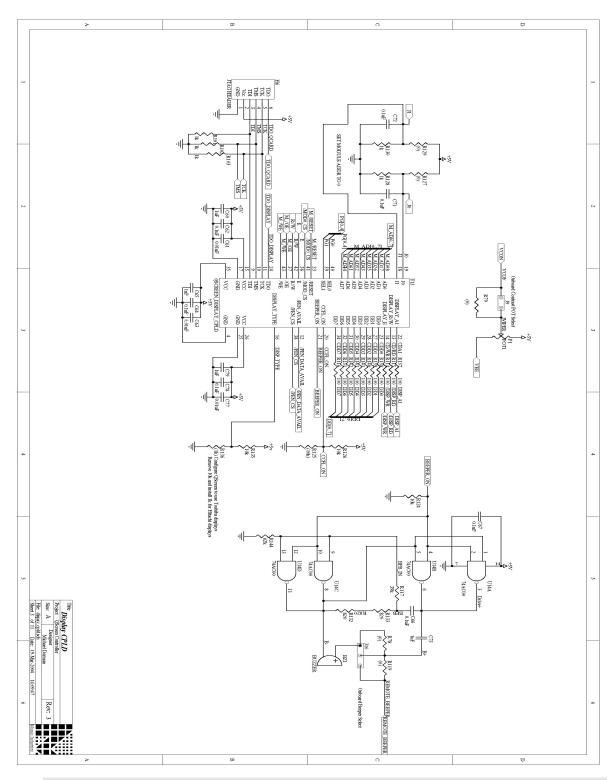


Figure C-4 QScreen Display CPLD.

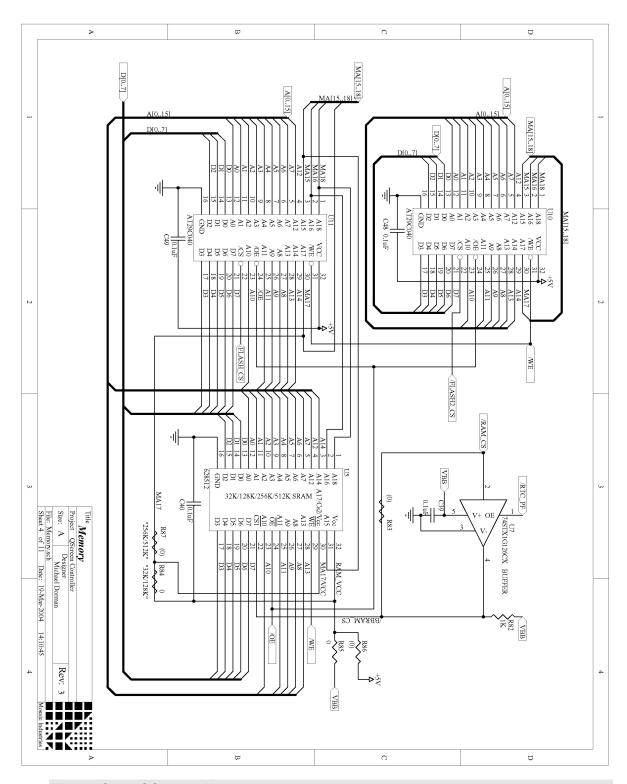


Figure C-5 QScreen Memory.

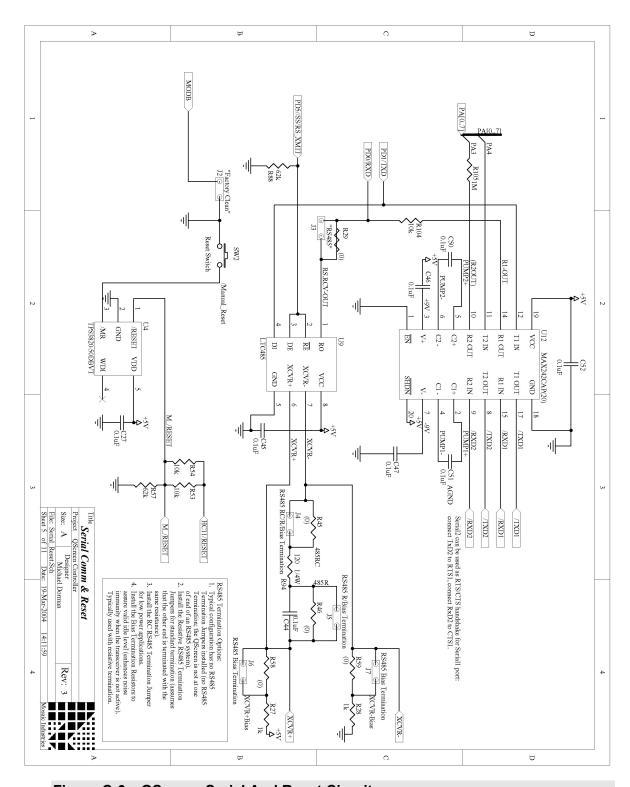


Figure C-6 QScreen Serial And Reset Circuitry.

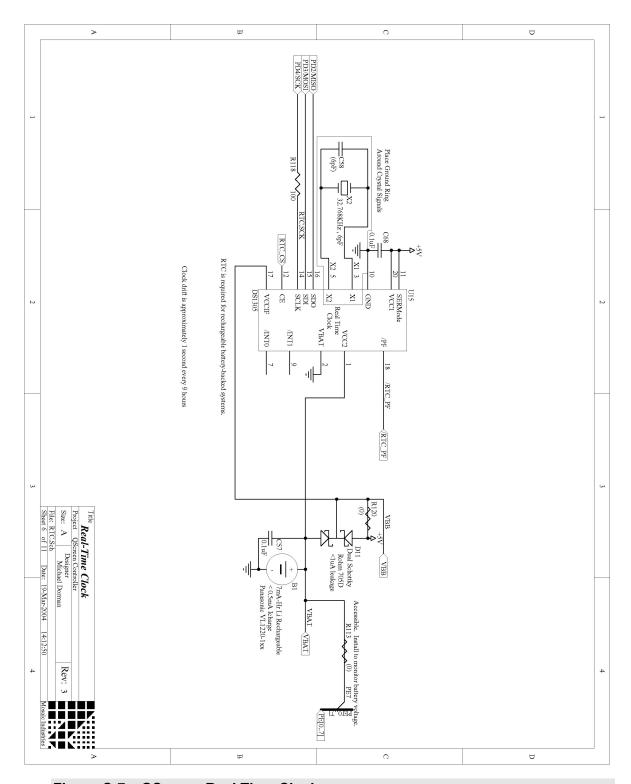


Figure C-7 QScreen Real Time Clock.

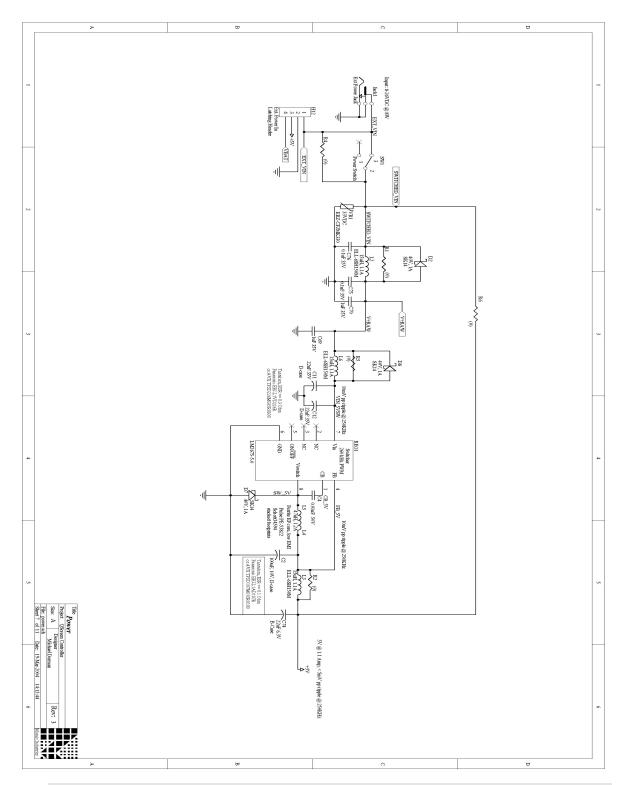


Figure C-8 QScreen Power.

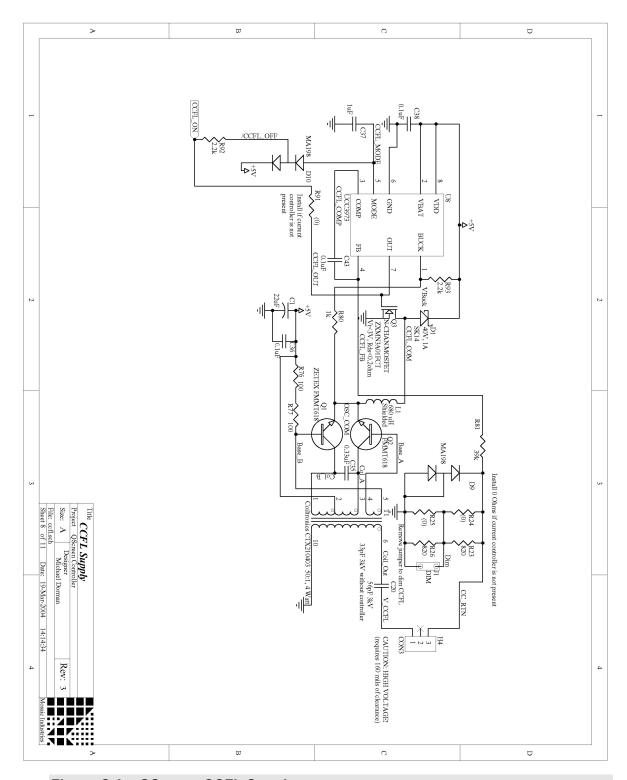


Figure C-9 QScreen CCFL Supply.

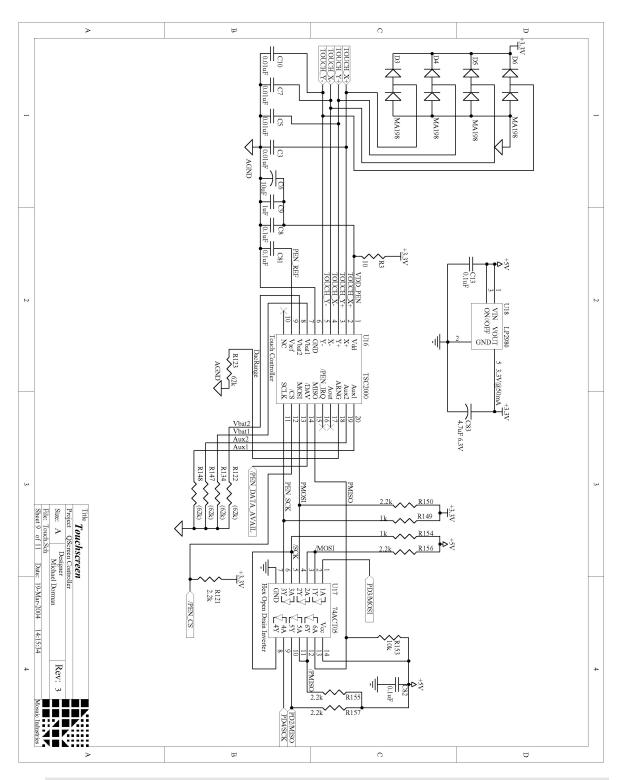


Figure C-10 QScreen Touchscreen.

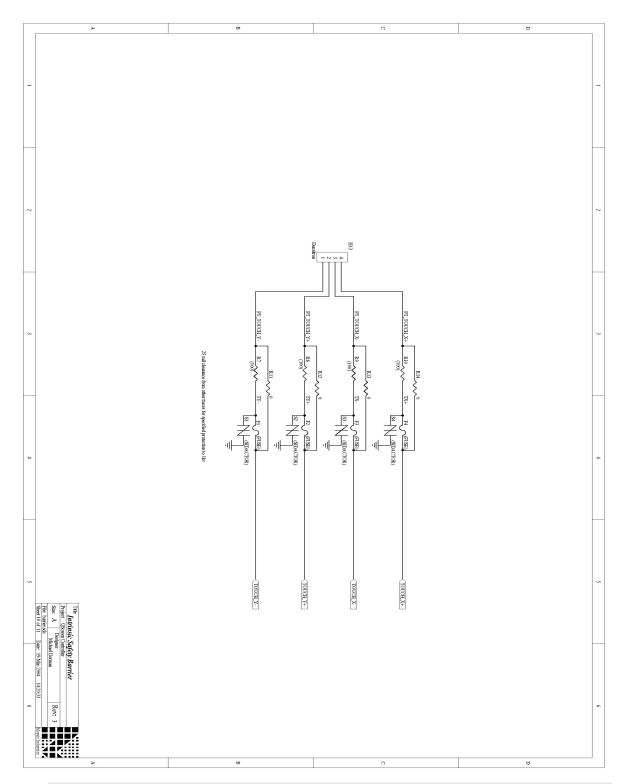


Figure C-11 QScreen Intrinsic Safety Barrier.

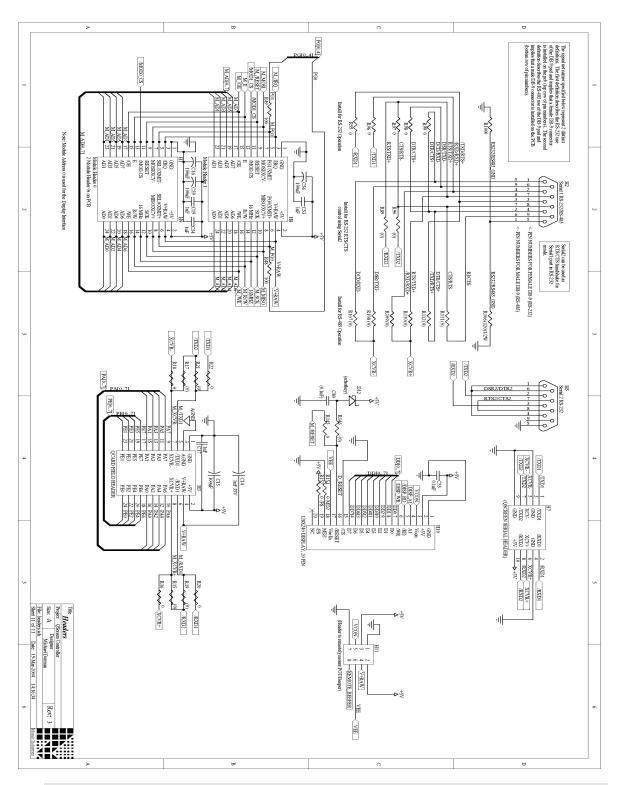


Figure C-12 QScreen Headers.